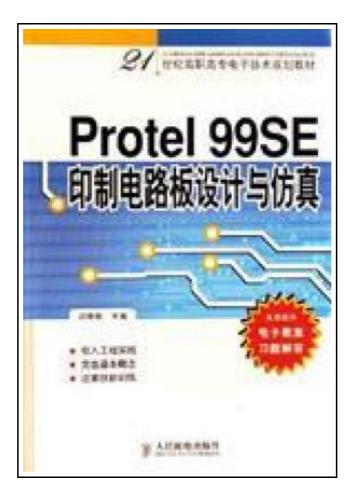
# Protel 99SE printed circuit board design and simulation



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## Reviews

Very useful to all of group of folks. I could possibly comprehended every little thing using this created e book. You wont truly feel monotony at anytime of your time (that's what catalogs are for concerning in the event you ask me).

(Claire Carroll DVM)

#### PROTEL 99SE PRINTED CIRCUIT BOARD DESIGN AND SIMULATION



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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Pages Number: 246 Publisher: Posts & Telecom Press Pub. Date: 2006-12-1. Protel 99SE book presents basic circuit schematic design. printed circuit board design. circuit simulation and signal analysis of four parts. Contents: Chapter 1 Introduction 1.1 Protel99SE Protel99SE base composition 1.1.2 Protel99SE 1.1.1 Protel99SE runtime environment 1.2 Protel99SE operating environment and characteristics of special database management environment 1.2.1 1.2.2 1.2.3 Schematic PCB design environment board design environment 1.3 Protel99SE Group Management 1.4 Protel99SE the design of the basic operation 1.4.1 Protel99SE of startup and shutdown 1.4.2 Environment 1.4.3 Protel99SE into Protel99SE design document management training exercises on the machine Chapter 2 schematic design environment settings 2.1 window settings 2.1.1 View menu command to switch the component environment 2.1.2 Design Manager 2.1.3 parts manager to switch switch to switch the status bar 2.1.4 2.1.5 2.1.6 Tools command bar switch column switching paper sizes 2.2 2.2.1 2.2.2 drawings drawings set the direction of the color drawings 2.2.3 2.3.1 Grid and set the cursor to set the grid to set the cursor 2.4 2.3.2 Other settings 2.4.1 DocumentOptions in the system font settings 2.4.3 2.4.2 Document Organization on the machine screen resolution training exercises in Chapter 3 the schematic design schematic design tool 3.2 3.1 schematic components. libraries and libraries of the switching element manager using 3.2.1 3.2 3.2.3 .2 element manager interface management library 3.3 physical placement and wire editing 3.3.1 (Wire) 3.3.2 bus (Bus) 3.3.3 component (Part) 3.3.4 Network label (NetLabel) 3.3.5 Power and ground (PowerPort) 3.3.6 node (Junction) 3.3.7 3.3.8 ignore the text and graphics ERC level circuit design. test point 3.4 3.4.1 3.4.2 box to place the circuit box. circuit access points 3.4.3 Circuit input and output...



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